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AN INTERNATIONAL INTELLECTUAL PROPERTY LAW FIRM

FAX TRANSMISSION February 16, 2006

TO: Mail Stop: AMENDMENT
Examiner: Kimberly E. Glenn
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10873.0935USC1

TELEPHONE: (612) 455.3800

Total pages, including cover letter: 8


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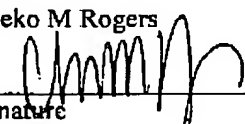
Title of Document: **COMMUNICATION REGARDING FORM 1449**

Applicant: MAEDA ET AL.
Serial No.: 10/805072
App. Filed: March 19, 2004
Group Art No.: 2817

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By: 
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Reg. No.: 30,300

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Chieko M Rogers

Signature

February 16, 2006
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S/N 10/805,072

FEB 16 2006

PATENT

Notice of Allowance Date: February 28, 2005

Confirmation No.: 7915

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	MAEDA, ET AL.	Examiner:	K. E. GLENN
Serial No.:	10/805,072	Group Art Unit:	2817
Filed:	March 19, 2004	Docket No.:	10873.0935USC1
Title:	HIGH-FREQUENCY SEMICONDUCTOR DEVICE		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on February 16, 2006.

By: 

Name: Chieko Rogers

COMMUNICATION REGARDING FORM 1449

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the Office Communication mailed February 6, 2006, paper Number 013106. With respect to the Oh-Hashi et al. reference cited in this application, Applicants respectfully assert that a copy of the reference was submitted in the Information Disclosure Statement filed on May 27, 2005. A new copy of the reference is submitted herewith for the Examiner's convenience. Applicants respectfully request that the Form 1449, marked as being considered by the Examiner be returned to the undersigned, and that this information be printed on the patent.

Respectfully submitted,

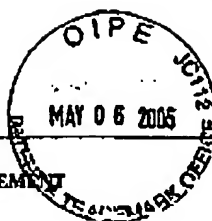
Hamre, Schumann,
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Date: February 16, 2006By: 

Name: Douglas P. Mueller
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DPM/cmr

Date Mailed: May 3, 2005

Sheet 1 of 1



FORM 1449*

INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number:

10873.935USC1

Application Number:

10/805,072

Applicant: MAEDA, et al.

Filing Date: March 19, 2004

Group Art Unit: 2817

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>JS</i>	5,955,926	September, 1999	Uda, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
<i>JS</i>	57-138421	February, 1981	Japan			Partial	
	6-6151	January, 1994	Japan			Abstract	
	7-263634	October, 1995	Japan			Abstract	
	7-283668	October, 1995	Japan			Abstract	
	7-307626	November, 1995	Japan			Abstract	
	9-139639	May, 1997	Japan			Abstract	
	10-163772	June, 1998	Japan				
	11-355015	December, 1999	Japan			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>JS</i>		Oh-Hashi, et al., An Analysis Method of a Tapered-line Power Divider Using an Equivalent Circuit with Series of Small Uniform Transmission-Lines and Resistors, Proceedings of the 1999 IEEE General Conference, The Institute of Electronics, Information and Communication Engineers, March 1999, page 103 (partial translation)

52835

PATENT TRADEMARK OFFICE

EXAMINER

Samuel J. Sherr

DATE CONSIDERED

1/31/06

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.